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LIST OF REFERENCES CITED BY APPLICANT

(Use as many sheets as necessary)

Complete if Known

Application Number: 09/873,110
Filing Date: June 1, 2001
First Named Inventor: Eiji OKI
Group Art Unit: Not yet assigned
Examiner Name: Not yet assigned

Sheet	1	of	1

Attorney Docket No.: Poly-21/APP

OTHER REFERENCES - NON-PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No.[1]	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume, issue number(s), publisher, country, where published, source.	T[2]
JNM	AA.	J. Turner and N. Yamanaka, "Architectural Choices in Large Scale ATM Switches", IEICE Trans. Commun., Vol. E81-B, No. 2, pp. 120-137 (Feb. 1998).	
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	AK.	A. Smiljanic, "Flexible Bandwidth Allocation in Terabit Packet Switches", pp. 233-239.	

Examiner Signature	JNM	Date Considered	10/17/04

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

1 Unique citation designation number. 2 Applicant is to place a check mark here if English language translation is attached.